

FIG 1

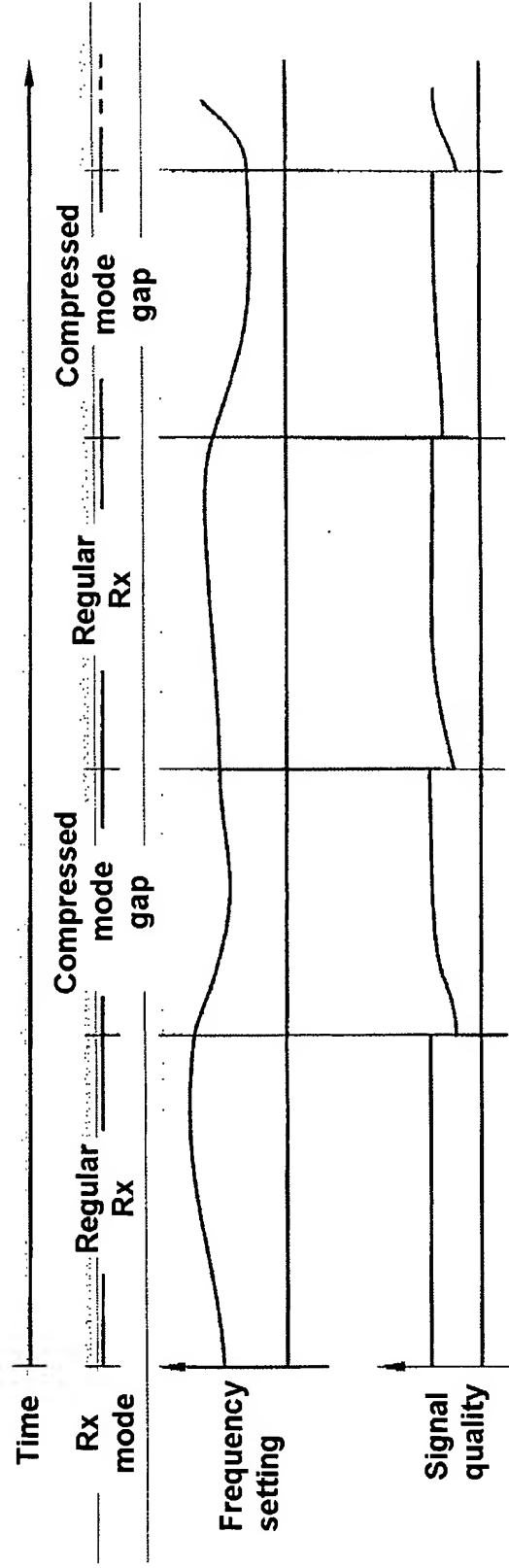


FIG 2

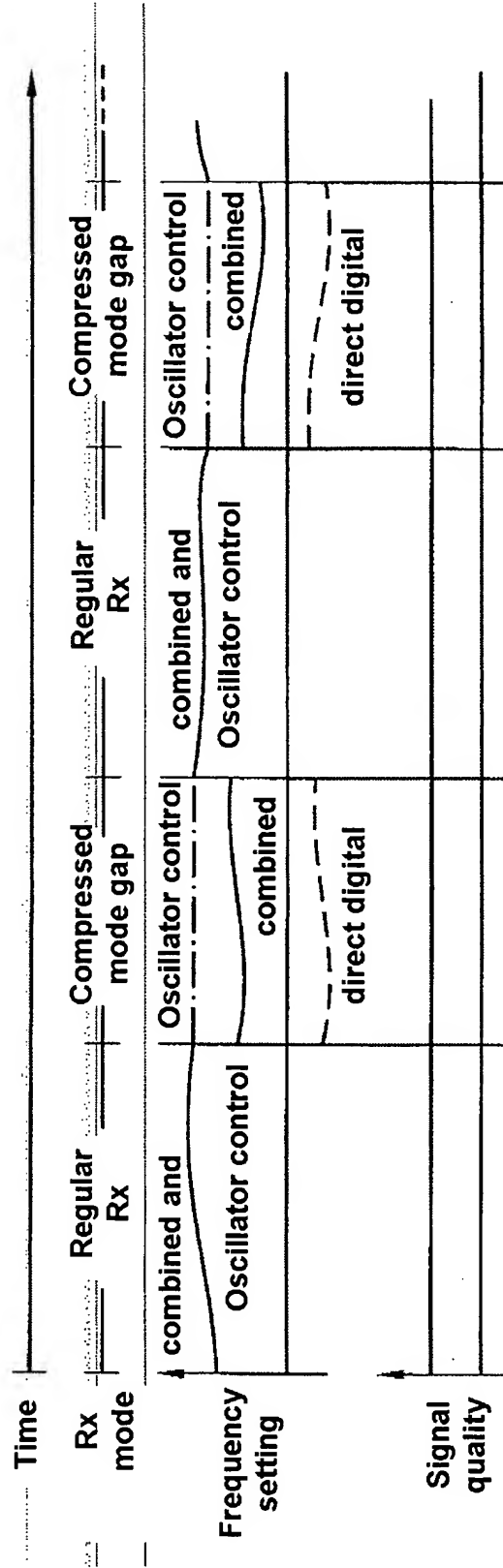


FIG 3

Block diagram of a receiver system (FIG 3) showing external components and a chip (100).

EXTERNAL COMPONENTS:

- TRANSMISSION CHIP (9):** Connected to the external components.
- BASIS (8):** A block that receives input from the transmission chip and outputs to the chip (100).
- PLL (15) and VCO (16):** A phase-locked loop and voltage-controlled oscillator block that receives input from the chip (100) and outputs to the chip (100).

CHIP (100):

- Block 12:** Receives input from the external components and outputs to block 11.
- Block 11:** Receives input from block 12 and outputs to block 10.
- Block 10 (AFC):** Receives input from block 11 and outputs to block 10.
- Block 10 (CORDIC):** Receives input from block 10 and outputs to block 22.
- Block 22:** Receives input from block 10 and outputs to block 21.
- Block 21 (ADC + EC):** Receives input from block 22 and outputs to block 20.
- Block 20:** Receives input from block 21 and outputs to block 23.
- Block 23 (RRC filter):** Receives input from block 20 and outputs to block 30.
- Block 30:** Receives input from block 23 and outputs to block 31.
- Block 31:** Receives input from block 30 and outputs to block 30.

The diagram is divided into two sections: **RS1** (Receiver Section 1) and **RS2** (Receiver Section 2).

